

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. SAITO et al.

Serial No.: (new continuation application under 37 CFR §1.53[b] of U.S.
Application Serial No. 09/613,138)

Filed: March 11, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND FABRICATION PROCESS THEREOF

Group: 2814

Examiner: P. CAO

CLAIM FOR FOREIGN PRIORITY

MS: PATENT APPLICATIONS

Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

March 11, 2004

Sir:

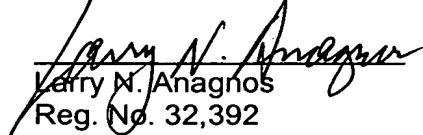
Pursuant to 35 U.S.C. §119 and 37 CFR §1.55, applicants hereby claim
the right of priority based on the following foreign application filed in Japan:

Application No. 9-234236, filed August 29, 1997

Application No. 10-182813, filed June 29, 1998

Certified copies of these foreign applications were filed on August 25,
1998 in U. S. application Serial No. 09/139,410, filed on August 25, 1998.

Respectfully submitted,
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